

**SOLID STATE IMAGING DEVICE**

**Abstract of the Disclosure**

An image plane includes a plurality of pixels. Each pixel comprises a photodiode and two transistors, and each pixel is connected by a signal  
5 bus to a respective storage node located off the image plane. Each storage node comprises two capacitors and associated switches. One of the transistors applies a reset pulse to the pixel, and the other transistor connects the pixel to a given conductor of the signal  
10 bus, which is then connected to the storage node. The pixel transistors can be operated simultaneously, and the sensed values can subsequently be transferred from the storage nodes sequentially.

09993387 44604  
TOP SECRET